

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name and that I believe I am an original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

TESTING ARCHITECTURE FOR A SEMICONDUCTOR MEMORY DEVICE

the specification of w	hich					
X is attached here	eto.					
was filed on	- ··· · · ·					
as Application S		ar	d was amended on	<u> </u>	·	
specification, including I acknowledge application in accordance.	ng the claims the duty to contend the duty to contend the duty to contend the duty to contend the duty the duty the claims are duty to claims.	s, as amen disclose in tle 37, Cod ority benefi or inventor nt or inver	ded by ar formation le of Fede its under s certifica	which is material teral Regulations, § Title 35, Untied S te listed below and	rred to above. to the patentab 1.56(a). tates Code, § have also ide	oility of this 119 of any
Number	Country		Date Filed(yyyy/mm/dd)		Yes	No
						Х
I hereby appoint the following transact all business in the Patern Jiawei Huang Maria Erlinda C. Sarno Belinda Lee		-			ewith: (Reg. No ong (Reg. No	. 39,081)
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COMBINED DECLARATION AND POWER OF ATTORNEY CONTINUED

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.

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